



P-Channel 20-V (D-S) MOSFET with Schottky Diode

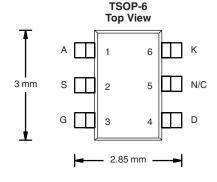
PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A)			
- 20	0.200 at V _{GS} = - 4.5 V	± 1.8			
	0.340 at V _{GS} = - 2.5 V	± 1.3			

SCHOTTKY PRODUCT SUMMARY					
V _{KA} (V)	I _F (A)				
20	0.48 V at 0.5 A	0.5			

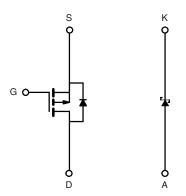
FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- LITTLE FOOT[®] Plus
- Compliant to RoHS Directive 2002/95/EC





Ordering Information: Si3853DV-T1-E3 (Lead (Pb)-free) Si3853DV-T1-GE3 (Lead (Pb)-free and Halogen-free)



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T_A	= 25 °C, unl	ess otherwis	e noted			
Parameter	Symbol	5 s	Steady State	Unit		
Drain-Source Voltage (MOSFET and Schottky)	V_{DS}	-				
Reverse Voltage (Schottky)		V_{KA}	20		V	
Gate-Source Voltage (MOSFET)		V _{GS}	± 12	± 12		
Continuous Dusin Comment /T 450 °C\ (MOCETT\)	T _A = 25 °C	- I _D	± 1.8	± 1.6		
Continuous Drain Current (T _J = 150 °C) (MOSFET) ^a	T _A = 70 °C		± 1.5	± 1.2		
Pulsed Drain Current (MOSFET)		I _{DM}	± 7		٨	
Continuous Source Current (MOSFET Diode Conduction) ^a		I _S	- 1.05	- 0.75	Α	
Average Forward Current (Schottky)	I _F	0.5				
Pulsed Foward Current (Schottky)	I _{FM}	7				
M : B B: : : (MOOFFT)3	T _A = 25 °C		1.15	0.83		
Maximum Power Dissipation (MOSFET) ^a	T _A = 70 °C	P _D	0.73	0.53	W	
Marrian David Discipation (Calcattly)	T _A = 25 °C	' D	1.0	0.76		
Maximum Power Dissipation (Schottky) ^a	T _A = 70 °C		0.64	0.48		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	

Notes:

a. Surface mounted on 1" x 1" FR4 board.

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THERMAL RESISTANCE RATINGS							
Parameter		Device	Symbol	Typical	Maximum	Unit	
Junction-to-Ambient ^a	t ≤ 5 s	MOSFET	. R _{thJA}	93	110		
		Schottky		103	125		
	Steady State	MOSFET		130	150	°C/W	
		Schottky		140	165	C/VV	
Junction-to-Foot	Steady State	MOSFET	R _{thJF}	75	90		
		MOSFET		80	95		

Notes:

a. Surface mounted on 1" x 1" FR4 board.

MOSFET SPECIFICATIONS T _J = 25 °C, unless otherwise noted								
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit		
Static								
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = -250 \mu A$	- 0.5			V		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			± 100	nA		
Zava Cata Valtaga Dvain Current		V _{DS} = - 16 V, V _{GS} = 0 V			- 1			
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -16 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 75 ^{\circ}\text{C}$		- 10		μΑ		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	- 5			Α		
Drain Cauras On State Besistance	В	V _{GS} = - 4.5 V, I _D = - 1.8 A		0.160	0.200	0		
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 2.5 V, I _D = - 1.0 A		0.280	0.340	Ω		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 5 V, I _D = - 1.8 A		3.6		S		
Diode Forward Voltage ^a	V _{SD}	I _S = - 1.05 V, V _{GS} = 0 V		- 0.83	- 1.10	V		
Dynamic ^b								
Total Gate Charge	Qg			2.7	4.0			
Gate-Source Charge	Q_{gs}	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -1.8 \text{ A}$		0.4		nC		
Gate-Drain Charge	Q_{gd}			0.6				
Turn-On Delay Time	t _{d(on)}			11	17			
Rise Time	t _r	$V_{DD} = -10 \text{ V}, R_{L} = 10 \Omega$		34	50			
Turn-Off DelayTime	t _{d(off)}	$I_D \cong -1 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_g = 6 \Omega$		19	30	ns		
Fall Time	t _f]		24	36			
Body Diode Reverse Recovery Time	t _{rr}	I _F = - 1.05 A, dl/dt = 100 A/μs		20	40			

Notes:

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

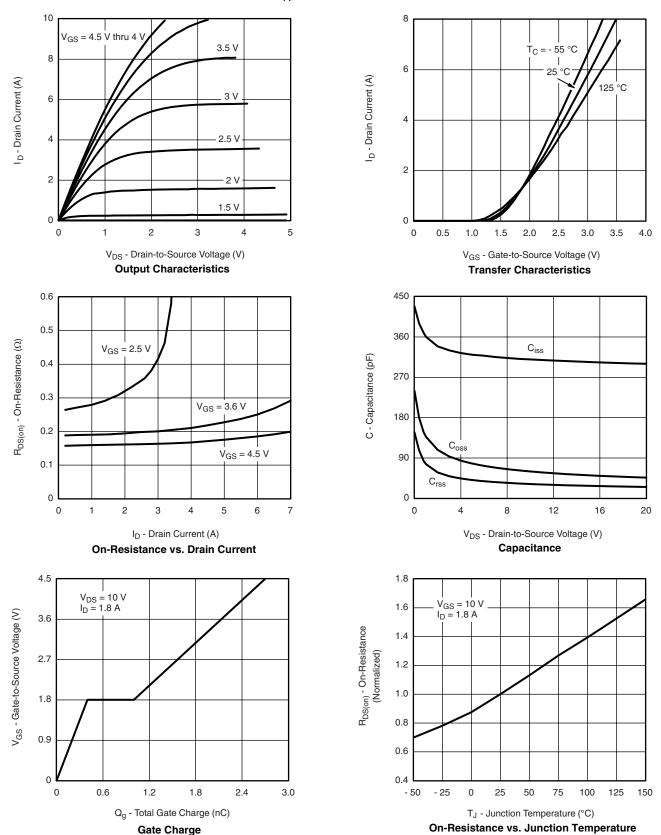
SCHOTTKY SPECIFICATIONS T _J = 25 °C, unless otherwise noted							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Forward Voltage Drop	V _F	I _F = 0.5 A		0.42	0.48	V	
		I _F = 0.5 A, T _J = 125 °C		0.33	0.4		
Maximum Reverse Leakage Current	I _{rm}	V _R = 20 V		0.002	0.100		
		V _R = 20 V, T _J = 75 °C		0.06	1	mA	
		V _R = 20 V, T _J = 125 °C		1.5	10		
Junction Capacitance	C _T	V _R = 10 V		31		pF	

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.





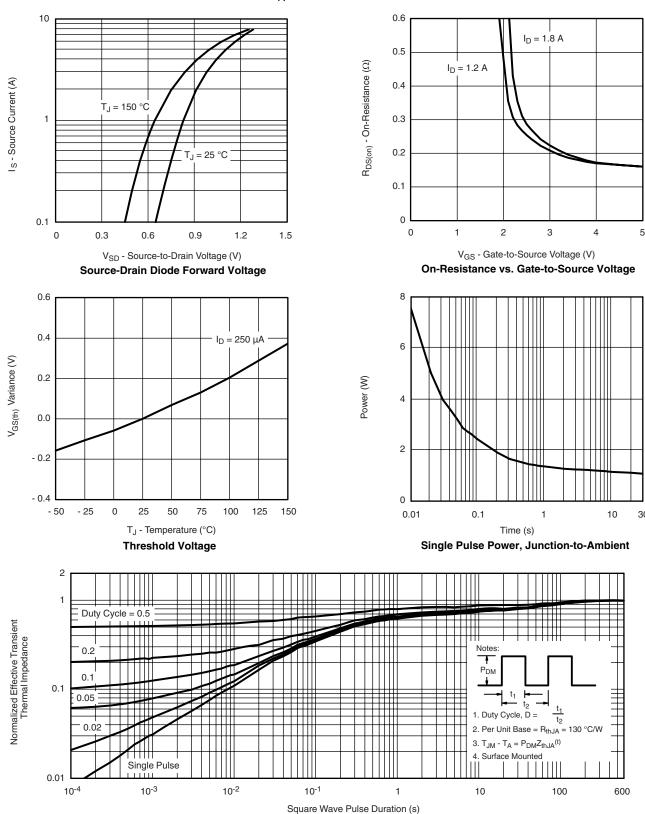
MOSFET TYPICAL CHARACTERISTICS $T_A = 25~^{\circ}C$, unless otherwise noted



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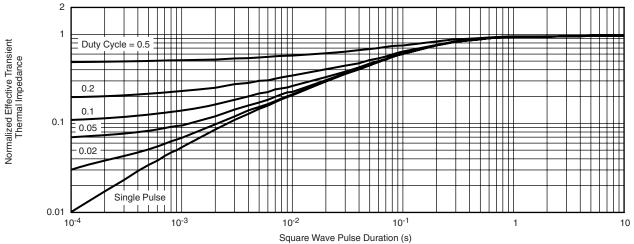
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MOSFET TYPICAL CHARACTERISTICS $T_A = 25$ °C, unless otherwise noted



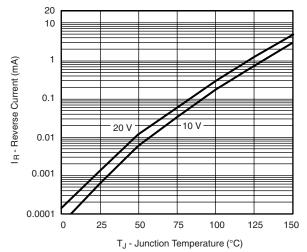


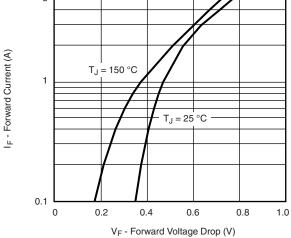
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Normalized Thermal Transient Impedance, Junction-to-Foot

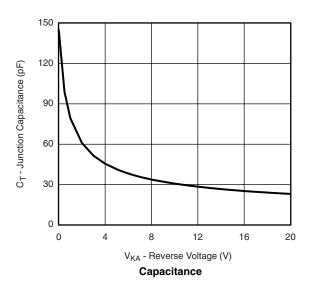
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Reverse Current vs. Junction Temperature

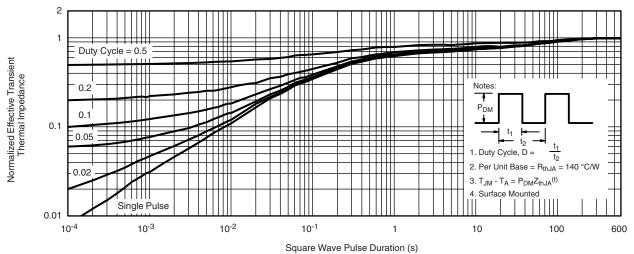




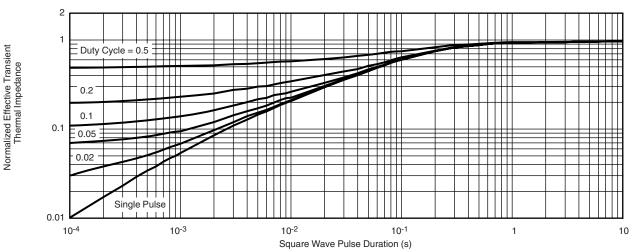
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SCHOTTKY TYPICAL CHARACTERISTICS $T_A = 25~^{\circ}C$, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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